

**S/N 10/629,279**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Robert D. Norman et al. Examiner: John J. Tabone  
Serial No.: 10/629,279 Group Art Unit: 2117  
Filed: July 29, 2003 Docket: 703.079US4  
Title: APPARATUS AND METHOD FOR DETECTING OVER-PROGRAMMING  
CONDITION IN MULTISTATE MEMORY DEVICE

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**COMMUNICATION CONCERNING RELATED APPLICATION(S)**

**MS Issue Fee**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date/Issue Date</u>	<u>Attorney Docket</u>	<u>Title</u>
08/991,216 6,278,632	December 12, 1997	703.059US2	METHOD AND CIRCUITRY FOR PERFORMING ANALOG OVER-PROGRAM AND UNDER-PROGRAM DETECTION FOR A MULTISTATE MEMORY CELL
09/328,075 6,163,479	June 8, 1999	703.059US3	METHOD FOR PERFORMING ANALOG OVER-PROGRAM AND UNDER-PROGRAM DETECTION FOR A MULTISTATE MEMORY CELL
09/652,802 6,577,532	August 31, 2000	703.059US4	SYSTEM FOR PERFORMING ANALOG OVER-PROGRAM AND UNDER-PROGRAM DETECTION FOR A MULTISTATE MEMORY CELL
08/736,195 5,771,346	October 24, 1996	703.079US1	APPARATUS AND METHOD FOR DETECTING OVER-PROGRAMMING CONDITION IN MULTISTATE MEMORY DEVICE
09/031,391 6,112,314	February 26, 1998	703.079US2	APPARATUS AND METHOD FOR DETECTING OVER-PROGRAMMING CONDITION IN MULTISTATE MEMORY DEVICE
09/641,693 6,601,191	August 18, 2000	703.079US3	APPARATUS AND METHOD FOR DETECTING OVER-PROGRAMMING CONDITION IN MULTISTATE MEMORY DEVICE

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CONDITION IN MULTISTATE  
MEMORY DEVICE

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,

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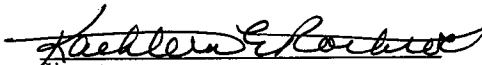


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**CERTIFICATE UNDER 37 CFR 1.8:** The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: MS Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 23 day of October, 2008.

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